

Second School on Runtime Verification, as part of the ArVi COST Action 1402

Overview and Reflections

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Abstract. This paper briefly reports on the second international school on Runtime Verification, co-organized and sponsored by Inria and COST Action IC1402 ArVi. The school was held March 19-21 2018, in Praz sur Arly (near Grenoble) in the French Alps. Most of the lectures dealt with introductory and advanced topics on Runtime Verification from the first tutorial book on Runtime Verification [2]. Additional lectures were given on cutting-edge research topics. We report the context and objectives of the school, overview its program, and propose outlooks for the future editions of the school.

1 Context and Objectives

Runtime Verification (RV) is the umbrella term to refer to the study of languages, (lightweight) techniques, and tools related to the verification of the executions of software and hardware systems against behavioral properties (see [5,10,11,14,7,4] for tutorials and overviews). Runtime Verification is a very effective technique to ensure that a system is correct, reliable, and robust. Compared to other verification techniques, RV is more practical than exhaustive verification techniques (e.g., model-checking, static analysis), at the price of losing completeness. Compared to conventional testing, RV is more powerful and versatile.

As a field of research, RV is endowed with a yearly conference¹, which exists since 2000. The field is getting more mature and diverse and the community is building documentation and lecture material to help students and practitioners entering the field. The international school on Runtime Verification constitutes one key element to facilitate the adoption of RV. This edition of the school shared the same objectives as the first edition [6] (which was held in Madrid as part of RV 2016 [9]):

- to present the foundations of the techniques;
- to expose participants to cutting-edge advances in the field;
- to provide a balance on theoretical and practical aspects of Runtime Verification;
- to adopt a hands-on approach and expose participants with the basics of building an RV tool.

¹ runtime-verification.org.

Table 1. Program overview - Day 1 - Monday 19th March

Time slot	Topic	Lecturer
08:00 10:00	<i>An Introduction to Runtime Verification and Monitorability</i> slides video	A. Francalanza
10:30 12:00	<i>Monitoring Cyber-Physical Systems</i> slides video	A. Donze
13:00 14:30	<i>The Java Modeling Language a Basis for Static and Dynamic Verification</i> slides video	W. Ahrendt
14:30 15:30	<i>Foundations on Runtime Verification</i> video	M. Leucker
16:00 16:45	<i>Monitoring Data Minimization</i> slides video	G. Schneider
16:45 17:30	<i>Runtime Assertion-Based Verification for Hardware and Embedded Systems</i> slides video	L. Pierre

Table 2. Program overview - Day 2 - Tuesday 20th March

Time slot	Topic	Lecturer
08:00 10:00	<i>Combined static and dynamic analyses in Frama-C: An Overview</i> slides video	N. Kosmatov
10:30 12:00	<i>A Hands-On Introduction to Building a Runtime Verification Tool</i> slides video	C. Colombo
14:00 17:00	Social event: Outing to Mont Blanc	

2 Presentation

The second edition of the school on Runtime Verification was sponsored by COST Action IC1402 ArVi², Inria³, and Persyval-Lab⁴.

The school was organised over three days with a series of lectures from international experts (see Tables 1-3). Lectures at the school ranged from the fundamentals of runtime verification to more practical aspects, but also covered cutting-edge research. All lectures were fully recorded. In Tables 1-3, below each lecture title, one can find 3 clickable links to the slides and videos.

In the remainder, we report on some of the lessons learned from the organization of the school and make suggestions to future organizers of the school.

² www.cost-arvi.eu

³ www.inria.fr

⁴ persyval-lab.org

Table 3. Program overview - Day 3 - Wednesday 21st March

Time slot	Topic	Lecturer
08:00 10:00	<i>Discovering Concurrency Errors</i> slides video	J. Loureno
10:30 12:00	<i>Stream Runtime Verification</i> slides video	C. Sanchez
13:00 14:30	<i>Industrial Experiences with Runtime Verification of Financial Transaction Systems: Lessons Learnt and Standing Challenges</i> slides video	C. Colombo

3 Reflections

We summarize the most frequent comments obtained from the participants. The balance between practice and theory was really appreciated by the participants (with application-oriented lectures very welcome). However, we note that some participants (legitimately) found the practice sessions too short and that monitoring for concurrency errors was under-represented (only 2 hours). Participants appreciated the format of the sessions: lectures last between one and two hours and focused on a topic. However, some participants with experience in RV would have preferred to opt for slightly more technical lectures with a greater focus on state of the art approaches, especially during the last sessions (as was actually the case with the first edition of the school). The participants appreciated the opportunities to discuss with experts and lecturers (thanks to the long breaks and social events); these opportunities allowed new connections to participants. Some participants suggested organizing additional group discussions in the form of panels dedicated to cutting-edge topics lead by the lecturers.

4 Outlooks

Given the great success of the last two editions, we hope that the school will proceed in the future. We make recommendations for future editions of the school. These recommendations are based on the experience gained from the organization of the two editions and the feedback received from the participants.

- First, we recommend that future editions of the school last longer: 3 to 4 full days would be the appropriate duration to dedicate one day to basics, one day to advanced topics, one day to cutting-edge research and recent results, and one day dedicated to tool construction (which is of importance for such a pragmatic technique as RV).
- Regarding practice sessions, we believe that sessions such as the ones organized during the first edition of the school would be more effective by allowing students to really address implementation issues. To save time, setup and configurations of the tools involved should be provided to the participants ahead of time. Using technologies such as virtual machines and containers is to be considered.

- Moreover, we would like to see a lecture dedicated to tool evaluation, detailing the methodology to compare a tool and assess its relevance.
- Furthermore, we would suggest future organizers to prepare overview slides providing participants with a big picture of the presented techniques. This shall help connect methods, techniques, and tools, and better see the complementarities. Similar to this is the concern of participants in seeing when to use RV compared to other techniques. Providing concrete example situations (upfront during the first lecture) where non-RV techniques fail and where RV techniques are complementary would clarify the position of RV in the big picture of verification techniques.
- To put the approaches in a better perspective, it would be nice to compare different approaches addressing the same problem. After an exposition of the approaches, a common interactive session could serve see the limits of each approach and study possible cross-fertilization between them.
- Finally, we would like to organize a small competition between the tools constructed by the students, in the same spirit as the competitions on Runtime Verification, usually held during the conference [1,8,13,3,12].

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